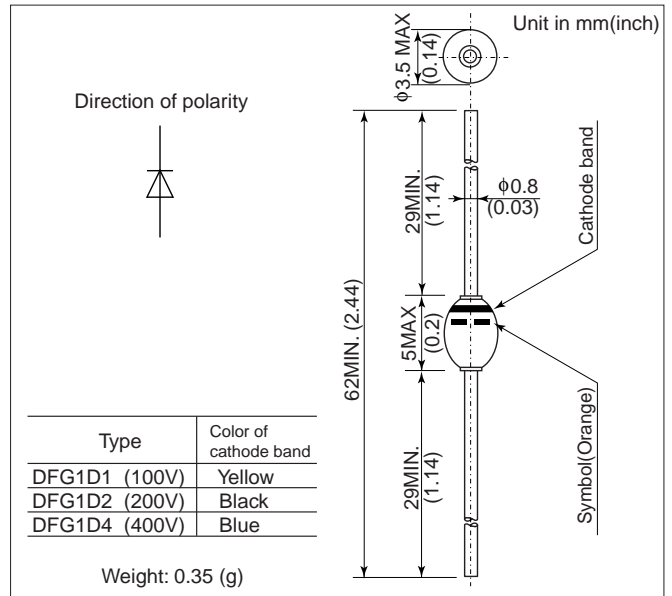


# DFG1D

## FEATURES

- For high speed switching.
- Diffused-junction. Glass passivated and encapsulated.

## OUTLINE DRAWING



## ABSOLUTE MAXIMUM RATINGS

Items	Type		DFG1D1	DFG1D2	DFG1D4
Repetitive Peak Reverse Voltage	$V_{RRM}$	V	100	200	400
Average Forward Current	$I_{F(AV)}$	A	1.0 (Single-phase half sine wave 180° conduction ) TL = 75°C, Lead length = 10mm		
Surge(Non-Repetitive) Forward Current	$I_{FSM}$	A	30 ( Without PIV, 10ms conduction, Tj = 150°C start )		
I <sup>2</sup> t Limit Value	I <sup>2</sup> t	A <sup>2</sup> s	3.6 ( Time = 2 ~ 10ms, I = RMS value )		
Operating Junction Temperature	T <sub>j</sub>	°C	-65 ~ +150		
Storage Temperature	T <sub>stg</sub>	°C	-65 ~ +150		

Notes (1) Lead mounting : Lead temperature 300°C max. to 3.2mm from body for 5sec. max..

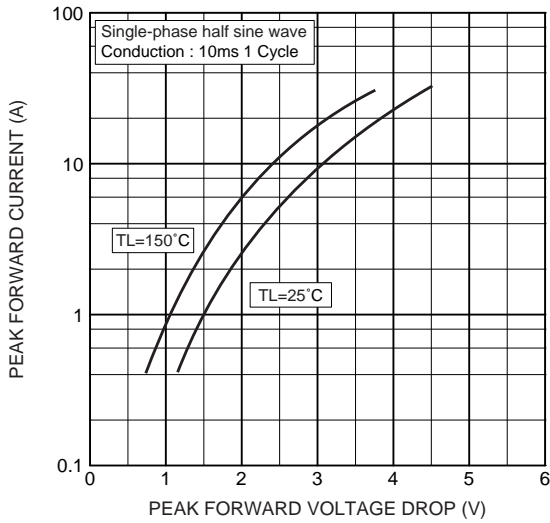
(2) Mechanical strength : Bending 90°×2 cycles or 180°×1 cycle, Tensile 2kg, Twist 90°×1 cycle.

## CHARACTERISTICS(T<sub>L</sub>=25°C)

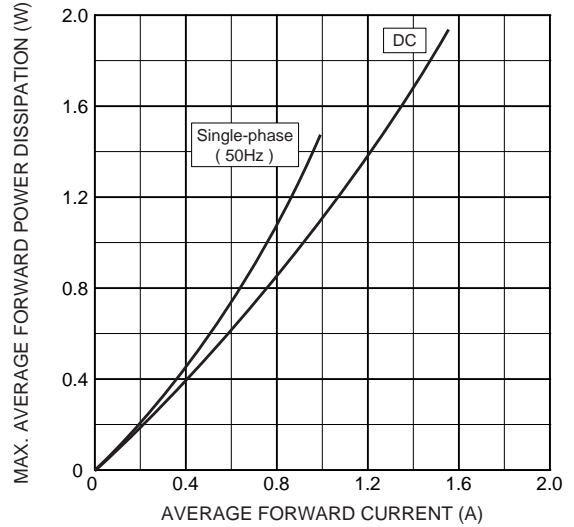
Items	Symbols	Units	Min.	Typ.	Max.	Test Conditions
Peak Reverse Current	$I_{RRM}$	μA	—	—	10	Rated $V_{RRM}$
Peak Forward Voltage	$V_{FM}$	V	—	—	1.5	$I_{FM}=1.0A_p$ , Single-phase half sine wave 1 cycle
Reverse Recovery Time	$t_{rr}$	ns	—	—	50	$I_F=0.5A$ , $I_{rp}=1.0A$ , 25% Recovery
Steady State Thermal Impedance	$R_{th(j-a)}$	°C/W	—	—	80	Lead length = 10 mm
	$R_{th(j-l)}$				50	

# DFG1D

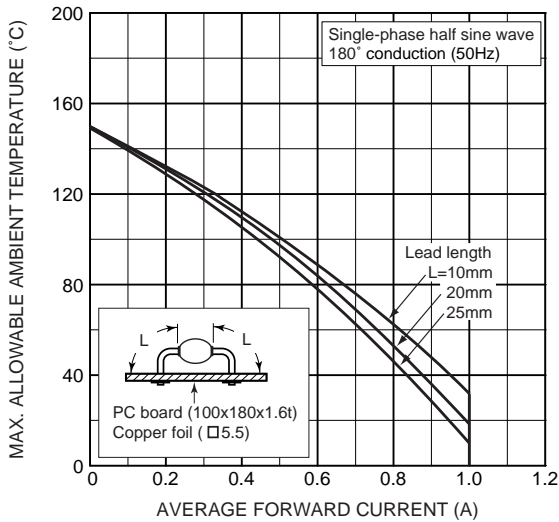
Forward characteristics



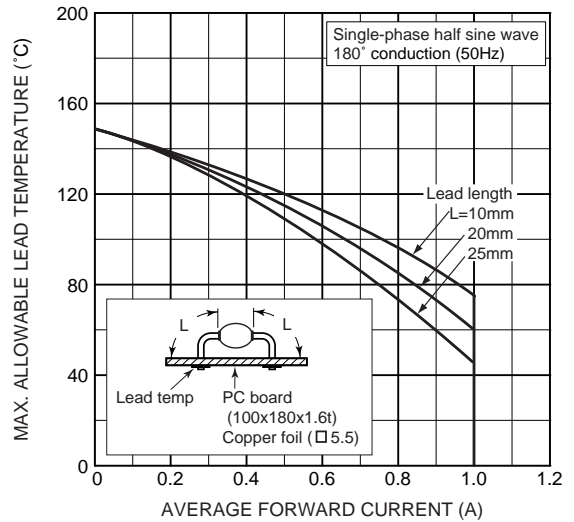
Max. average forward power dissipation (Resistive or inductive load)



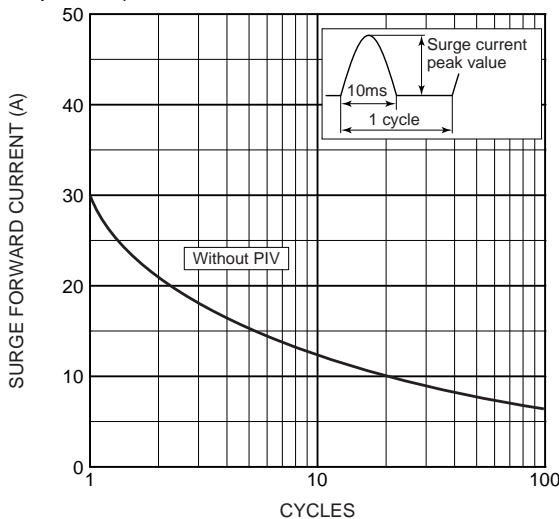
Max. allowable ambient temperature (Resistive or inductive load)



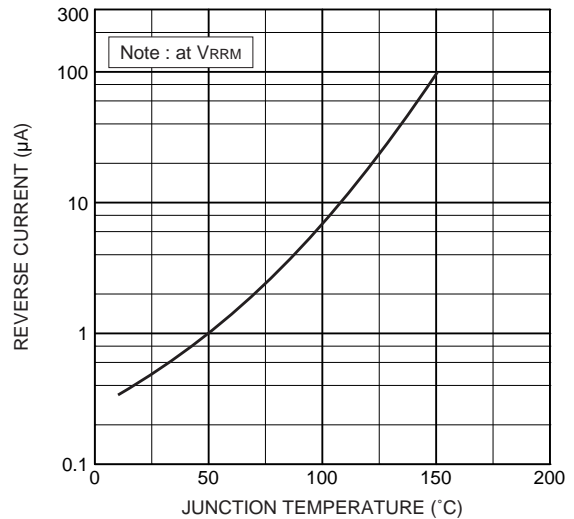
Max. allowable lead temperature (Resistive or inductive load)



Surge forward current characteristics (Non-repetitive)

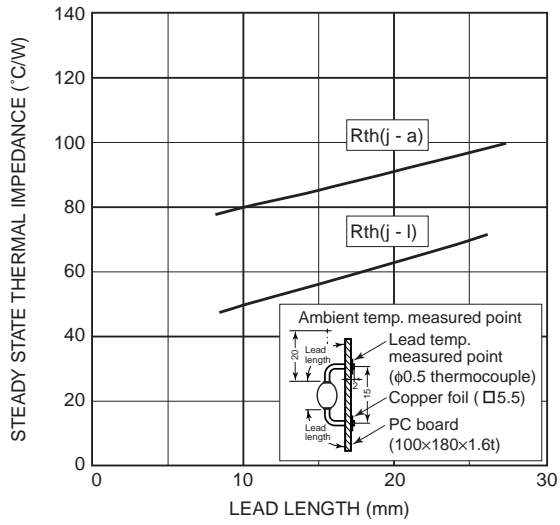


Typ. Reverse current vs. junction temperature

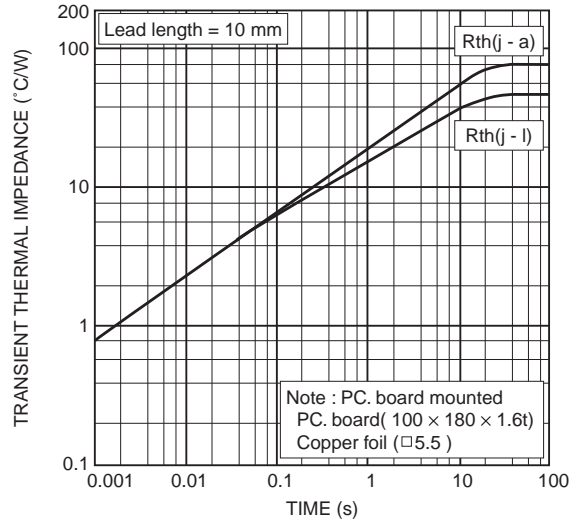


# DFG1D

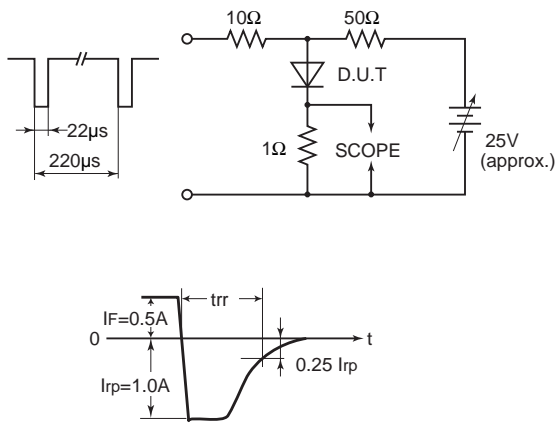
## Steady state thermal impedance



## Transient thermal impedance



## Reverse recovery time ( $t_{rr}$ ) test circuit



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## Precautions for Safe Use and Notices

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If semiconductor devices are handled in inappropriate manner, failures may result.  
For this reason, be sure to read "Precaution for Use" before use.



This mark indicates an item about which caution is required.



### CAUTION

This mark indicates a potentially hazardous situation which, if not avoided, may result in minor or moderate injury and damage to property.



### CAUTION

- (1) Regardless of changes in external conditions during use "absolute maximum ratings" should never be exceed in designing electronic circuits that employ semiconductors. In the case of pulse use, furthermore, "safe operating area(SOA)" precautions should be observed.
- (2) Semiconductor devices may experience failures due to accident or unexpected surge voltages. Accordingly, adopt safe design features, such as redundancy or prevention of erroneous action, to avoid extensive damage in the event of a failure.
- (3) In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, life-support-related medical equipment, fuel control equipment and various kinds of safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of user's fail-safe precautions or other arrangement. Or consult Hitachi's sales department staff.

(If a semiconductor device fails, there may be cases in which the semiconductor device, wiring or wiring pattern will emit smoke or cause a fire or in which the semiconductor device will burst)

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## NOTICES

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1. This Datasheet contains the specifications, characteristics(in figures and tables), dimensions and handling notes concerning power semiconductor products (hereinafter called "products") to aid in the selection of suitable products.
2. The specifications and dimensions, etc. stated in this Datasheet are subject to change without prior notice to improve products characteristics. Before ordering, purchasers are advised to contact Hitachi's sales department for the latest version of this Datasheet and specifications.
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<http://www.hitachi-power-semiconductor-device.co.jp/en/>