Silicon N-channel IGBT 3300V F version

FEATURES

- * Soft switching behavior, low switching loss & low conduction loss :
 - Soft low-injection punch-through
 - Advanced Trench High conductivity IGBT.
- * Low driving power due to low input capacitance with trench MOS gate.
- * Low noise recovery: Ultra soft fast recovery diode.
- * High Current rate Package.
- * Low $R_{th(j-c)}$ & low stray inductance.
- * RoHS
- * High thermal fatigue durability: (delta Tc=70K, N>30,000cycles)

ABSOLUTE MAXIMUM RATINGS (T_C=25°C)

	(•	. /		
Item		Symbol	Unit	MBN1800F33F
Collector Emitter Voltage		V _{CES}	V	3,300
Gate Emitter Voltage		V _{GES}	V	±20
Collector Current	DC	Ic	Λ	1,800
	1ms	I _{CRM}	1 A	3,600
Forward Current	DC	l _F	Λ	1,800
	1ms	I _{FRM}	A	3,600
Junction Temperature	•	T _{vj op}	°C	-50 ~ +150
Storage Temperature		T _{stg}	°C	-55 ~ +150
Isolation Voltage		V _{ISO}	V _{RMS}	6,000(AC 1 minute)
Screw Torque	Terminals (M4/M8)	-	N·m	2/15 (1)
	Mounting (M6)	-	111-111	6 (2)

Notes: (1) Recommended Value 1.8±0.2/15⁺⁰-3N·m (2) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

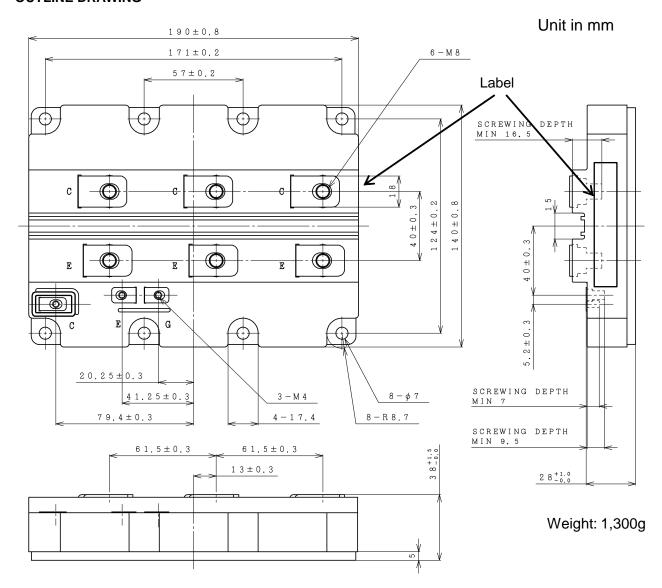
Item		I Unit	Min.	Typ.	Max.	Test Conditions
		Л	-	-	0.6	V _{CE} =3,300V, V _{GE} =0V, T _{vj} =25°C
Collector Emitter Cut-Off Currer	nt I _{CES}	mA	-	40	100	V _{CE} =3,300V, V _{GE} =0V, T _{vi} =150°C
Gate Emitter Leakage Current		nA	-500	-	+500	$V_{GE}=\pm20V$, $V_{CE}=0V$, $T_{vj}=25$ °C
Collector Emitter Saturation Voltage		V	2.5	2.85	3.5	I _C =1,800A, V _{GE} =15V, T _{vj} =150°C
Gate Emitter Threshold Voltage		V	5.5	6.5	7.5	V _{CE} =10V, I _C =1,800mA, T _{vj} =25°C
Input Capacitance		nF	-	132	-	$V_{CE}=10V$, $V_{GE}=0V$, $f=100kHz$, $T_{vj}=25^{\circ}C$
Internal Gate Resistance		Ω	-	1.3	-	V _{CE} =10V, V _{GE} =0V, f=100kHz, T _{vj} =25°C
Turn On Delay Time			-	0.8	-	V _{CC} =1,800V, I _C =1,800A
Rise Time		μS	-	0.3	-	L _S =80nH
Turn Off Delay Time		μδ	-	2.2	-	$R_G(\text{on/off})=4.7\Omega/5.6\Omega$ (3)
Fall Time			-	1.8	-	$V_{GE}=\pm 15V, T_{vj}=150^{\circ}C$
Peak Forward Voltage Drop		V	2.2	2.6	2.9	$I_F=1,800A, V_{GE}=0V, T_{vj}=150^{\circ}C$
Reverse Recovery Time		μS	-	0.7	-	V _{CC} =1,800V, I _F =1,800A, L _S =80nH T _{Vi} =150°C
Turn On Loss		J/P	-	3.7	-	V _{CC} =1,800V, I _C =1,800A, L _S =80nH
Turn Off Loss		J/P	-	3.3	-	$R_G(\text{on/off})=4.7\Omega/5.6\Omega$ (3)
Reverse Recovery Loss		J/P	-	2.4	-	$V_{GE}=\pm 15V$, $V_{ij}=150$ °C
Short Circuit Pulse Width		0	10	-		V _{CC} =2,000V, Ls=80nH
		μS	10		_	$R_G(on/off) = 4.7/56\Omega, V_{GF} = \pm 15V, T_{vj} = 150^{\circ}C$
Stray inductance module		nΗ	-	7	-	
Thermal Impedance IGBT	R _{th(j-c)}	K/W	-	-	0.0067	Junction to case
FVVD	R _{th(j-c)}	1	-	-	0.012	Junction to case
Contact Thermal Impedance	R _{th(c-f)}	K/W	-	0.005	-	Case to fin

Notes: (3) R_G value is a test condition value for evaluation, not recommended value. Please, determine the suitable R_G value by measuring switching behaviors.

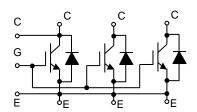
- * Please contact our representatives at order.
- * For improvement, specifications are subject to change without notice.
- * For actual application, please confirm this spec sheet is the newest revision.
- * ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2 and IEC 60747-9.

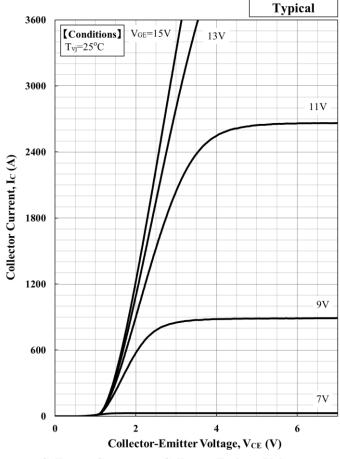


OUTLINE DRAWING

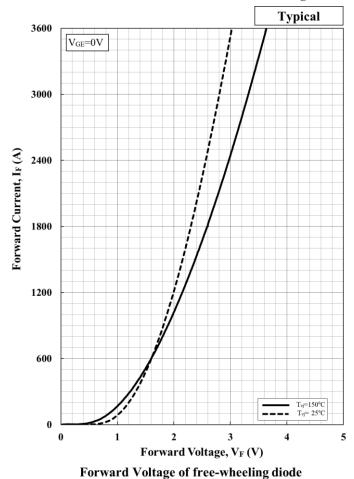


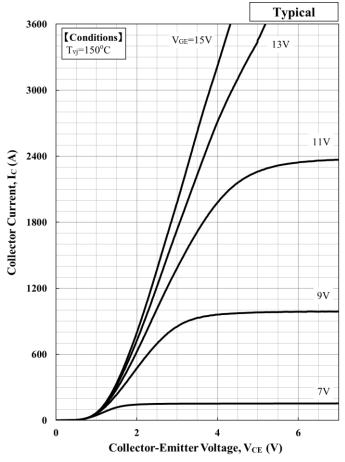
CIRCUIT DIAGRAM



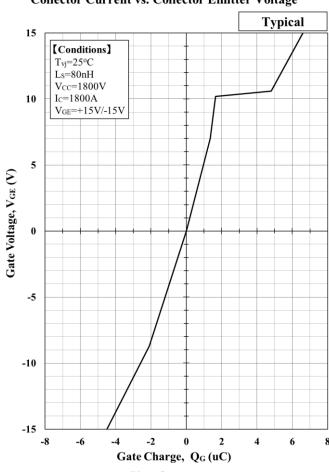


Collector Current vs. Collector Emitter Voltage



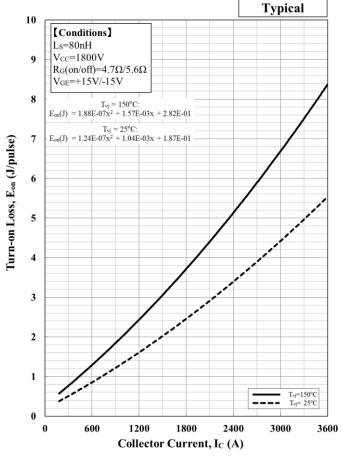


Collector Current vs. Collector Emitter Voltage

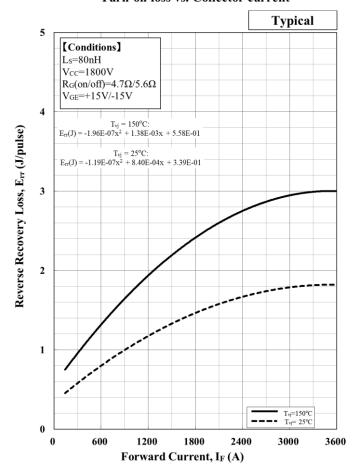


V_{GE}-Q_G curve HITACH

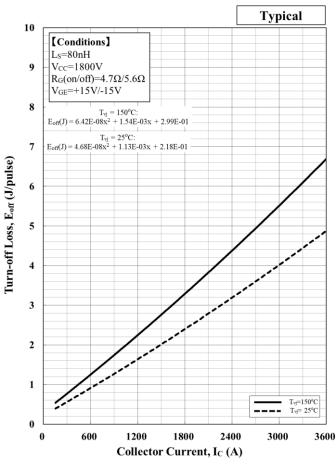
Inspire the Next



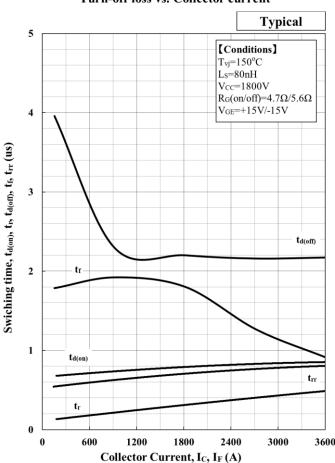
Turn-on loss vs. Collector current



Recovery loss vs. Forward current



Turn-off loss vs. Collector current

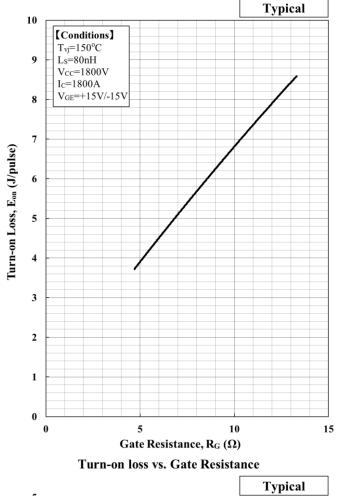


Switching time vs. Collector Current HITACHI

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Typical

MBN1800F33F



4 V_{GE}=+15V/-15V

3

1

0

5

10

15

Gate Resistance, R_G (Ω)

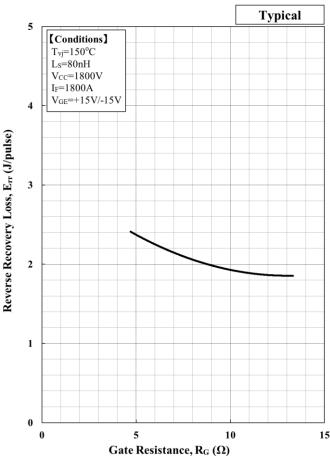
Turn-off loss vs. Gate Resistance

[Conditions]

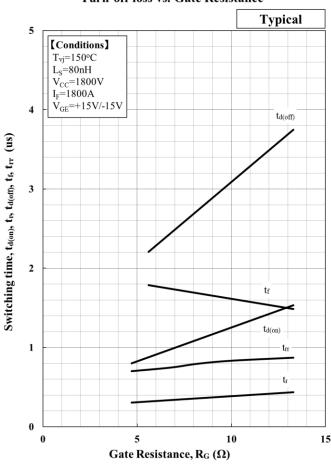
T_{vj}=150°C L_S=80nH

V_{CC}=1800V

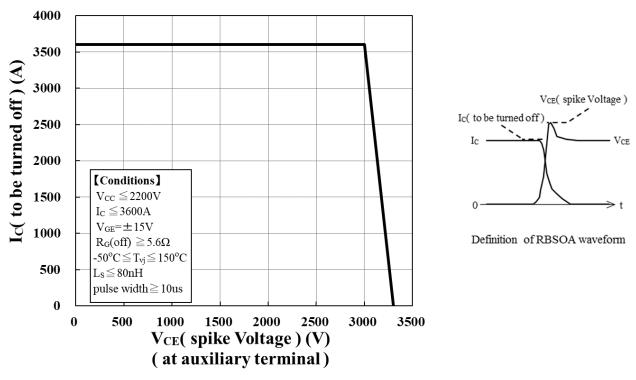
Ic=1800A



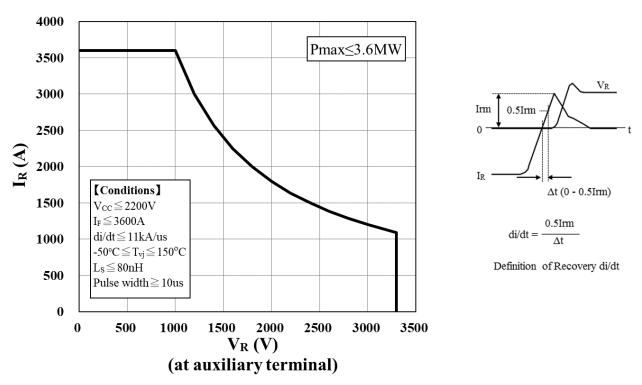
Recovery loss vs. Gate Resistance



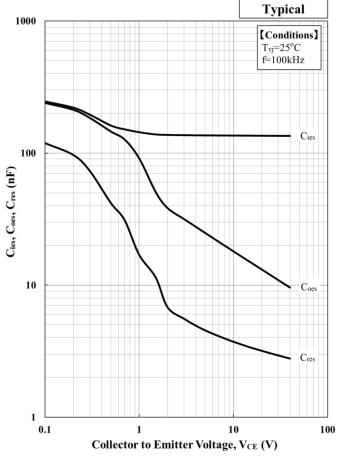
Switching time vs. Gate Resistance
HITACHI
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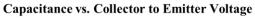


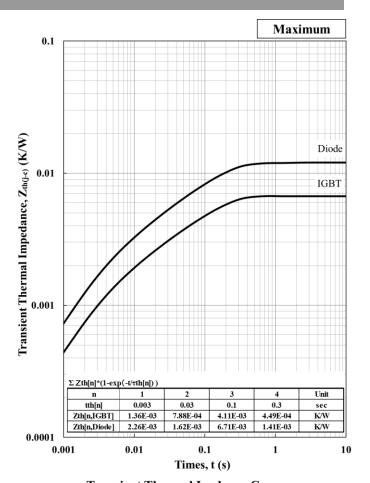
Reverse Bias Safe Operation Area (RBSOA)



Reverse Recovery Safe Operation Area (RRSOA)







Transient Thermal Ipedance Curve

HITACHI POWER SEMICONDUCTORS

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HITACHI POWER SEMICONDUCTORS

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