#### **FEATURES**

- \* Low noise due to soft and fast recovery diodes.
- \* High reliability, high durability diodes.
- \* Isolated heat sink(terminal to base).

#### **ABSOLUTE MAXIMUM RATINGS** (TC=25 °C)

	Item	Symbol	Unit	MDM1200E33D
Repetitive Peak Re	everse Voltage	$V_{RRM}$	V	3,300
Forward Current	DC	l <sub>F</sub>	۸	1,200
Forward Current	1ms	I <sub>FM</sub>	A	2,400
Junction Temper	ature	Tj	°C	-40 ~ +125
Storage Temper	ature	Tstg	°C	-40 ~ +125 (1)
Isolation Test	Terminals-base	erminals-base V <sub>ISO</sub>		6,000(AC 1 minute)
Voltage	Terminal 1-Terminal 2	V <sub>ISO T-T</sub>	V <sub>RMS</sub>	6,000(AC 1 minute)
Screw Torque	Terminals (M8)	-	N⋅m	15 (2)
Screw Forque	Mounting (M6)	IN·III	6 (3)	

(1) Terminal temperature shall not exceed the specified temperature in any operation. (2) Recommended Value 15<sup>+0</sup>/<sub>-3</sub>N·m (3) Recommended Value 5.5±0.5N·m Notes:

(3) Recommended Value 5.5±0.5N·m

#### **ELECTRICAL CHARECTERISTICS**

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Repetitive Reverse Current	I <sub>RRM</sub>	mA	-	3.0	30.0	VAK=3,300V, Tj=125°C
Forward Voltage Drop	$V_{F}$	V	2.3	2.8	3.3	IF=1,200A, Tj=125°C
Reverse Recovery Time	trr	μS	-	0.6	1.1	V <sub>CC</sub> =1,650V, IF=1,200A, L=100nH
Reverse Recovery Loss	E <sub>rr(10%)</sub>	J/P	-	1.2	1.9	Tj=125°C,Rg=3.3 $\Omega$ (4)

#### **PACKAGE CHARECTERISTICS**

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Terminal Resistance	RCE	mΩ	-	0.3	-	
Terminal Stray Inductance	LsCE	nΗ	-	35	-	
Thermal Impedance	Rth(j-c)	K/W	-	-	0.017	Junction to case
Comparative tracking index	CTI		-	600	-	
Contact Thermal Impedance	Rth(c-f)	K/W	-	0.008	-	Case to fin per module
Base Plate material			Al-SiC			
Insulation substrate material			AIN			

Notes:(4) Counter arm; MBN1200E33D VGE=+/-15V

R<sub>G</sub> value is the test condition's value for evaluation of the switching times, not recommended value. Please, determine the suitable R<sub>G</sub> value after the measurement of switching waveforms (overshoot voltage, etc.) with appliance mounted.

- \* Please contact our representatives at order.
- \* For improvement, specifications are subject to change without notice.
- \* For actual application, please confirm this spec sheet is the newest revision.



#### **DEFINITION OF TEST CIRCUIT**

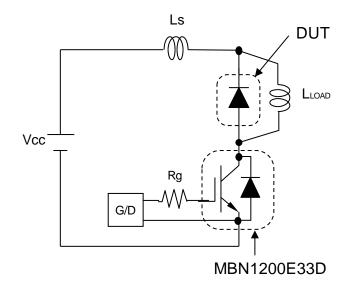


Fig.1 Switching test circuit

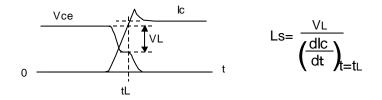


Fig.2 Definition of stray inductance

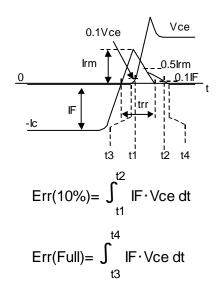


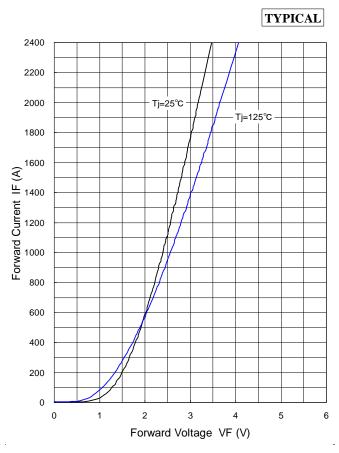
Fig.3 Definition of switching loss



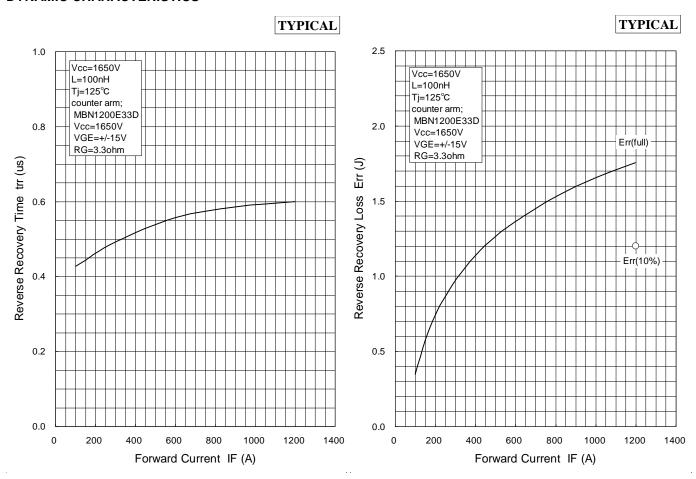
**DUAL DIODE MODULE** Spec.No.SR2-SP-05007R4 P3

### MDM1200E33D

#### STATIC CHARACTERISTICS

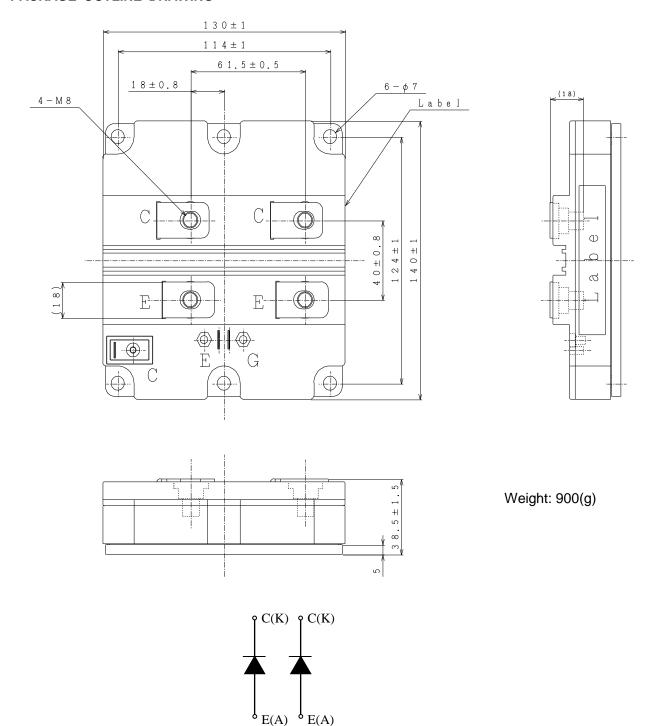


#### **DYNAMIC CHARACTERISTICS**





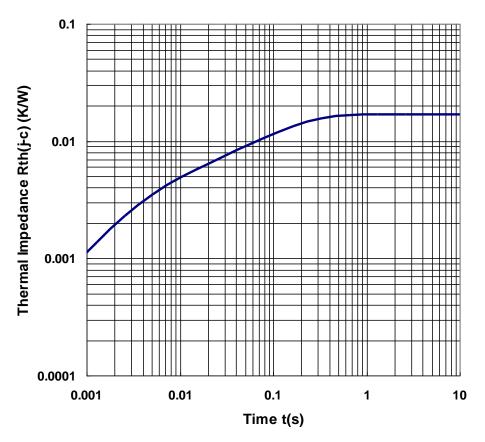
#### PACKAGE OUTLINE DRAWING



Circuit diagram



#### TRANSIENT THERMAL IMPEDANCE



Transient Thermal Impedance Curve (Maximum Value)

#### Material declaration

Please note the following materials are contained in the product, in order to keep characteristic and reliability level.

Material	Contained part
Lead (Pb) and its compounds	Solder



### HITACHI POWER SEMICONDUCTORS

### Notices

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